

## IOL-116-100-M08 manual

### 1. Connection diagram

As shown in Figure 1.

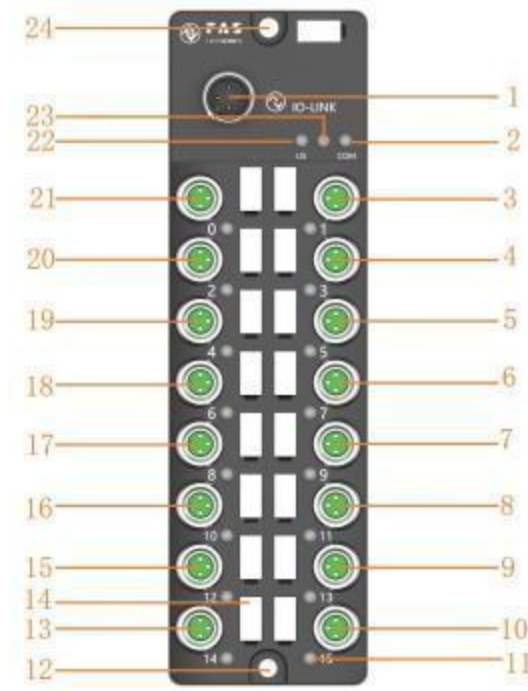


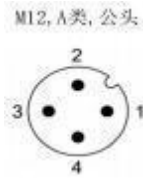
Figure 1

- |                                   |                          |    |
|-----------------------------------|--------------------------|----|
| 1. IO-Link interface              | 13 Standard output ports | 14 |
| 2. LED: Communication/Module      | 14 Labels                |    |
| 3. Standard output port 1         | 15 Standard output ports | 12 |
| 4. Standard output port 3         | 16 Standard output ports | 10 |
| 5. Standard output port 5         | 17 Standard output ports | 8  |
| 6. Standard output port 7         | 18 Standard output ports | 6  |
| 7. Standard output port 9         | 19 Standard output ports | 4  |
| 8. Standard output port 11        | 20 Standard output ports | 2  |
| 9. Standard output port 13        | 21 Standard output ports | 0  |
| 10. Standard output ports 15      | 22 LED: Controller power | US |
| 11. Port LED: Standard Input Port | 23 LED: Actuator Power   | UA |
| 15, Pin4                          | 24 Fixing hole           |    |
| 12 .Fixing hole                   |                          |    |

## 2. IO-Link interface diagram

As shown in Figure 2.

**M12, A coding, male**



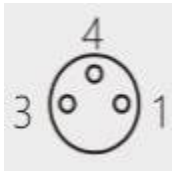
Pin	Function
1	Power Controller, +24 V, max2000 mA
2	NC
3	GND
4	C/Q, IO-Link data transmission channel

Figure 2

## 3. Standard Input Port Connection Diagram

As shown in Figure 3.

**M8 Female**



Pin	Function
1	+24 V, 200 mA
3	GND
4	Input signal

Figure 3

## 4. IO-Link data

### 4.1 Parameter

As shown in Table 1-1.

Surface 1-1

Data transmission baud rate	COM2 (38 4kbit/s)
Frame type	2 V
Minimum cycle time	2.2ms
Process data cycle time	2.2 ms, Consistent with minimum cycle time
Process data length	2

### 4.2 Process data/output data

No output data is defined.

## 4.3 Process data/input data

As shown in Figure 4.

Byte	1								0							
Bit	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0
Describe	Pin4	Pin4	Pin4	Pin4	Pin4	Pin4	Pin4	Pin4	Pin4	Pin4	Pin4	Pin4	Pin4	Pin4	Pin4	Pin4
	PORT 15	PORT 14	PORT 13	PORT 12	PORT 11	PORT 10	PORT 9	PORT 8	PORT 7	PORT 6	PORT 5	PORT 4	PORT 3	PORT 2	PORT 1	PORT 0

Figure 4

## 4.4 Parameter data/request data

As shown in Figure 5.

DPP	SPDU		Object name	length	Scope	Defaults
Index	Index	Subindex				
			Supplier ID	2		0x0454
			Device ID	3		0x627922
	0x10	0	Supplier name	18	Read only	FAS (Fujian) Co., LTD
	0x11	0	Supplier text	16		www.fas-elec.com
	0x12	0	Product name	20		FNI IOL-116-100-M08
	0x13	0	Product ID	6		00B516
	0x14	0	Product text	12		IO-Link Junction box 16DI NPN
	0x16	0	Hardware version	3		20210120
	0x17	0	Firmware version	3		2.01
	0x40	0	Bit reversal	2	0000-FFFF	0x0000

Figure 5

Note:

0x40 Set bit reverse: 0-bit is not reversed, 1-bit is reversed, such as external input is 0x0000, when 0x40 is 0x0000, the value is 0x0000 (not reversed), when 0x40 is 0xFFFF, the value is 0xFFFF (reverse).

## 4.5 Mistake

As shown in Figure 6.

Error code	Additional code
Device app error 0x80	Index unavailable 0x11
	Subindex unavailable 0x12
	Value out of range 0x30

Figure 6

## 4.6 Event

As shown in Figure 7.

Class/Qualifier			Code(High Bit + Low Bit)			
Pattern	Type	Instance				
Appear	Error	AL	Hardware	powered by	Power supply low voltage	U2=Power supply+24V
0xC0	0x30	0x03	0x5000	0x0100	0x0010	0x0002
0xF3			0x5112			
Disappear	Error	AL	Hardware	powered by	Power supply low voltage	U2=Power supply+24V
0x80	0x30	0x03	0x5000	0x0100	0x0010	0x0002
0xB3			0x5112			
Appear	Error	AL	Hardware	powered by	Peripheral power supply	
0xC0	0x30	0x03	0x5000	0x0100	0x0060	
0xF3			0x5160			
Disappear	Error	AL	Hardware	powered by	Peripheral power supply	
0x80	0x30	0x03	0x5000	0x0100	0x0060	
0xB3			0x5160			

Figure 7